

**CIRCUITS AND METHODS FOR REDUCING NOISE DISTORTION AND NOISE IN
PULSE WIDTH MODULATION SYSTEMS UTILIZING FULL – BRIDGE DRIVERS**

ABSTRACT:

[0040] A pulse width modulation circuit for driving a full – bridge output load includes a pulse width modulation stage for generating, from an input data stream, a pulse width modulated data stream for driving a terminal of a full – bridge output load and another pulse width modulated data stream for driving another terminal of the full bridge output load. A delay circuit delays the another pulse width modulated data stream relative to the pulse width modulated data stream such that edges of the another pulse width modulated data stream and edges of the pulse width modulated data stream are temporally spaced.